



ATHIRA SIVANANDHAN

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OBJECTIVE

Research and development in the areas of Embedded systems, Real time operating systems, and Control system. I would always like to bring out my best when I am involving myself in a competition.

ACADEMIC PROFILE

Doing PhD in Electronics and Communication from CHRIST University Bangalore

M.Tech in Embedded systems from Kerala university-CGPA-7.3

B.E in Electronics and Instrumentation from Anna University-CGPA-8.2

PROFESSIONAL EXPERIENCE

Assistant professor • MG University, India • July 2007 – till date

7years of experience

- Knowledge in Embedded system Security.
- Taught PLC and SCADA systems in undergraduate courses.
- Taught embedded system subjects such as RTOS, VLSI Design, digital signal processing, Application specific integrated circuits in undergraduate and graduate courses.
- Supervised student's projects in SCADA and PLC.

TECHNICAL SKILLS

- Knowledge in SCADA and PLC .
- **Programming languages:** Embedded C,C,C++
- **Programming Suit:** Matlab,Vx works,C,KEIL,MASM
- **Microprocessors and Microcontrollers:**8085,8086,8051,**ARMprocessor**XILINX FPGA board, DSP board
- **PLC** in ALLEN BRADLEY MicroLogix 1200 Series.

TEACHING SKILLS

<p style="text-align: center;">Subjects and Labs Handled</p>	<p>SCADA and PLC,ARM processor, Control Engineering, Modern Control Theory, Industrial and Biomedical Instrumentation, Instrumentation in process Industries, process control; power Electronics, Linear integrated circuits, Microcontrollers, DSP, ADSP.</p> <p>Process control lab, Electronic Circuits lab, Industrial Electronics Lab, Microprocessors and Microcontrollers Lab, Measurements lab, Industrial Instrumentation lab.</p>
<p style="text-align: center;">Responsibilities</p>	<p>University Examination coordinator, National service scheme Lady representative ,Main and mini project coordinator, Seminar coordinator, Women cell coordinator</p>
<p style="text-align: center;">Training</p>	<ul style="list-style-type: none"> ➤ Directorate of Technical Education (DTE) approved short term training program on Induction Training Program for Engineering College Teachers. ➤ Computing Simulation and Modeling in Electronics Engineering. ➤ Image Processing With Practical Applications. ➤ AICTE sponsored workshop on Universal Human Values. ➤ ATAL sponsored FDP on Artificial Intelligence

	➤ International Faculty development program on MICROSYSTEMS DESIGN and CONTROL ENGINEERING.
Tools	Xilinx system generator, Matlab, simulink, MASM, KEIL, ARM.

PROJECTS COMPLETED

- Implementation of Recursive Least Square Adaptive Filter Algorithm Using Xilinx FPGA.
- Implementation of Frequency Shift Keying using CORDIC algorithm.
- Microcontroller based Industrial load control.
- Microprocessor based Home Management System.

PAPER PUBLICATION

1. Implementation Of LMS and RMS adaptive noise cancellation Using Xilinx FPGA

International Journal of Science and Research

Volume 9, Issue 2, FEBRUARY 2020

2. Fractional Order PID Controller Using Recursive Least Square Algorithm in MIMO Systems.

International Journal of Science and Research

Volume 9 Issue 12, December 2020

ACADEMIC AWARDS AND ACHIEVEMENTS

- Good service entry award from Mount Zion College of Engineering.
- Department topper during Bachelor's program.
- Coordinator of women empowerment programs as part of women cell in Mount Zion College of Engineering.
- University Examination coordinator in Mount Zion College of Engineering.
- Member in The Indian Society for Technical Education (ISTE).

PERSONAL INFORMATION

Date of Birth : 26th April 1984

Gender : Female

Marital Status : Married

Nationality : Indian

REFERENCE

- Prof. Thomas George: Professor, Department of Applied electronics and Instrumentation
Mount Zion College of Engineering Pathanamthitta, Kerala.

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DECLARATION

I hereby declare that the above-mentioned information is correct up to my knowledge and I bear the responsibility for the correctness of the above-mentioned particulars.

Date: January 2021

Place: Pathanamthitta

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